

General Description

It combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. It is suitable for automotive application.

Features

- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

Application

- BLDC Motor driver
- DC-DC
- Load Switch

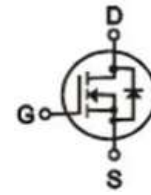
Ordering Information:

Part NO.	ZMS065N03N
Marking	ZMS065N03
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

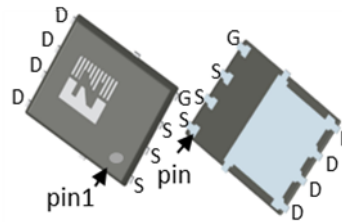
Absolute Maximum Ratings ($T_C=25^\circ\text{C}$)

Parameter	Symbol	Conditions	Value	Unit
Drain-Source Voltage	V_{DS}		30	V
Gate-Source Voltage	V_{GS}		± 20	V
Continuous Drain Current	I_D	$T_C=25^\circ\text{C}$	45	A
	I_D	$T_C=75^\circ\text{C}$	35	A
	I_D	$T_C=100^\circ\text{C}$	28	A
Pulsed Drain Current ^①	I_{DM}	Pulsed; $t_p \leq 10 \mu\text{s}$; $T_{mb} = 25^\circ\text{C}$;	180	A
Total Power Dissipation	P_D	$T_C=25^\circ\text{C}$	31	W
Total Power Dissipation	P_D	$T_A=25^\circ\text{C}$	2.8	W
Operating Junction Temperature	T_J		-55 to +150	$^\circ\text{C}$
Storage Temperature	T_{STG}		-55 to +150	$^\circ\text{C}$
Single Pulse Avalanche Energy	E_{AS}	$L=0.1\text{mH}$, $V_{GS}=10\text{V}$, $R_g=25\Omega$,	16.2	mJ
		$L=0.5\text{mH}$, $V_{GS}=10\text{V}$, $R_g=25\Omega$,	29.16	mJ
ESD Level (HBM)	CLASS 2			

Product Summary



$V_{DS} = 30\text{V}$
 $R_{DS(ON)} = 6.5\text{m}\Omega$
 $I_D = 45\text{A}$



DFN5*6



•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}		-	4	°C/W
Thermal resistance, junction-ambient	$R_{thJA\oplus}$		-	45	°C/W
Soldering temperature (total time<10s)	T_{sold}		-	260	°C

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	30			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.75	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{GS}=0V, V_{DS}=30V$			1.0	μA
Gate- Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$			100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$		6.5	8.1	m Ω
		$V_{GS}=4.5V, I_D=10A$		9.3	11.5	m Ω
Forward Transconductance	g_{FS}	$V_{GS}=5V, I_{SD}=5A$		11		S
Diode Forward Voltage	V_{FSD}	$V_{GS}=0V, I_{SD}=20A$			1.3	V

•Dynamic characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$f=1MHz, V_{DS}=25V$	-	640	-	pF
Output capacitance	C_{oss}		-	155	-	
Reverse transfer capacitance	C_{rss}		-	6	-	
Gate Resistance	R_g	$f=1MHz$	-	3.3		Ω
Total gate charge	Q_g	$V_{DD}=15V, I_D=20A, V_{GS}=10V$	-	10.8	-	nC
Gate - Source charge	Q_{gs}		-	2.1	-	
Gate - Drain charge	Q_{gd}		-	2.3	-	
Turn-ON Delay time	$t_{D(on)}$	$V_{GS}=10V, V_{DS}=15V, R_G=3.3\Omega, I_D=20A$	-	7	-	ns
Turn-ON Rise time	t_r		-	2.5	-	ns
Turn-Off Delay time	$t_{D(off)}$		-	18	-	ns
Turn-Off Fall time	t_f		-	4	-	ns
Reverse Recovery Time	t_{rr}	$V_{DD}=20V, di_S/dt=100A/\mu s, I_S=20A$	-	8	-	ns
Reverse Recovery Charge	Q_{rr}		-	14	-	nC

Fig.1 Gate-Charge Characteristics

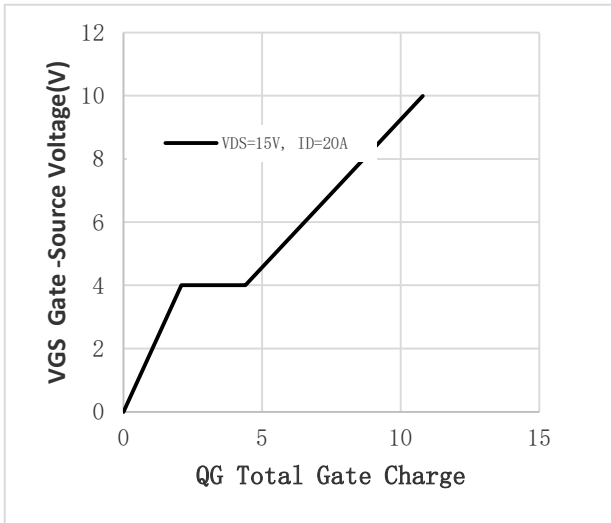


Fig.2 Capacitance Characteristics

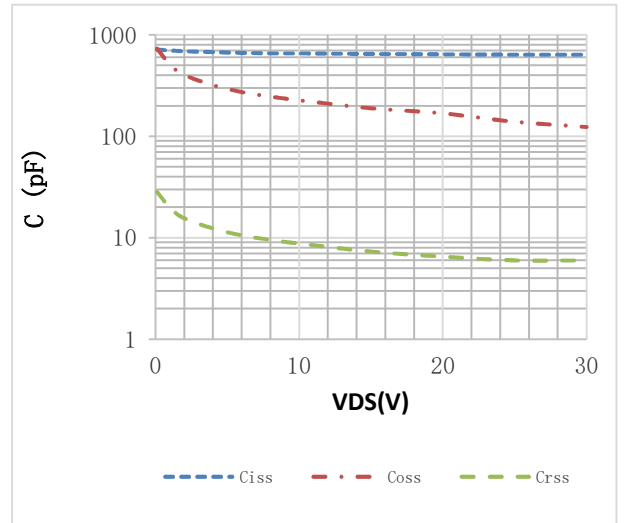


Fig.3 Power Dissipation

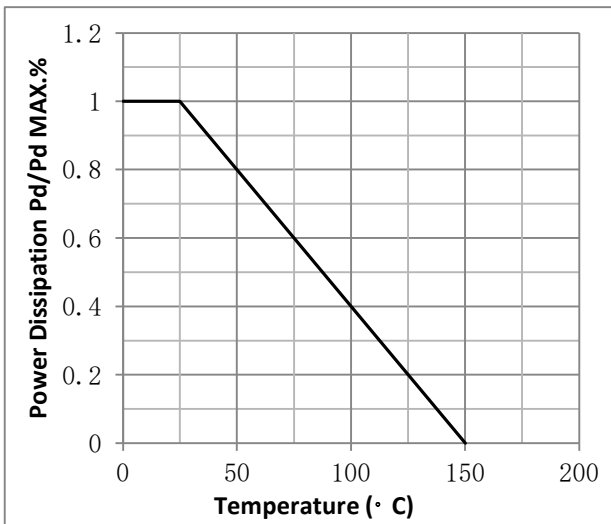


Fig.4 Typical output Characteristics

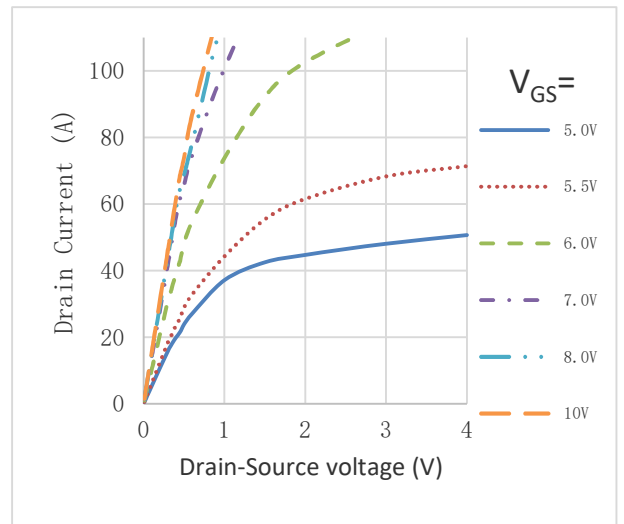


Fig.5 Threshold Voltage V.S Junction Temperature

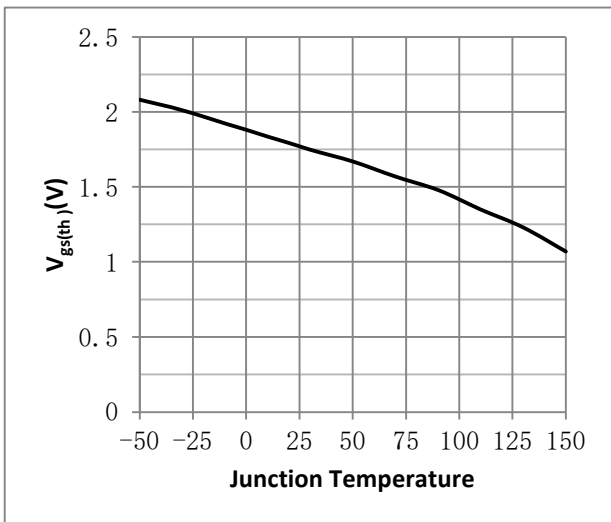


Fig.6 Resistance V.S Drain Current

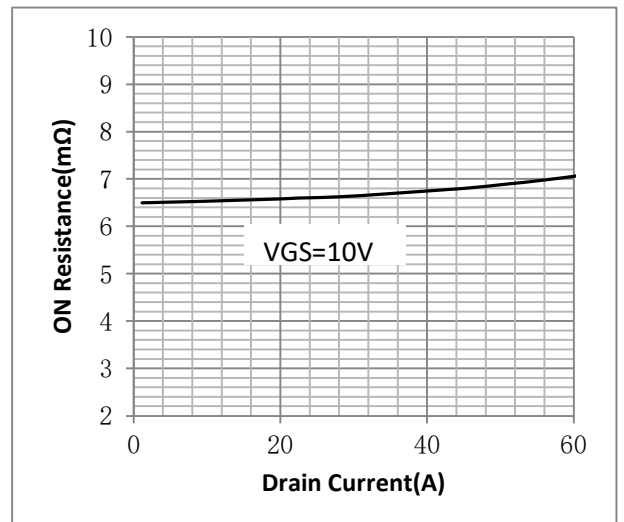


Fig.7 On-Resistance VS Gate Source Voltage

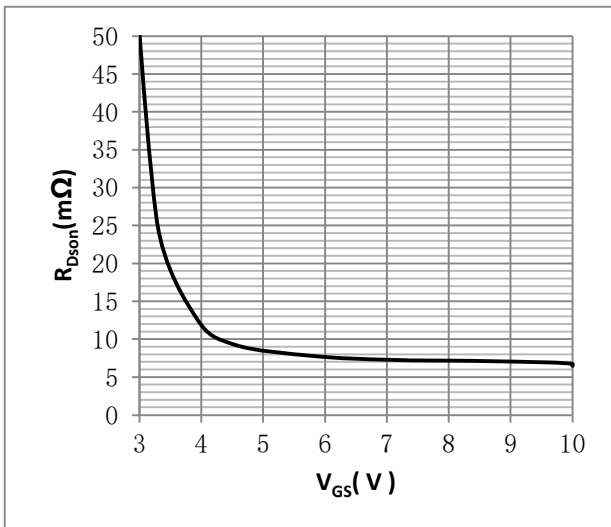


Fig.8 On-Resistance V.S Junction Temperature

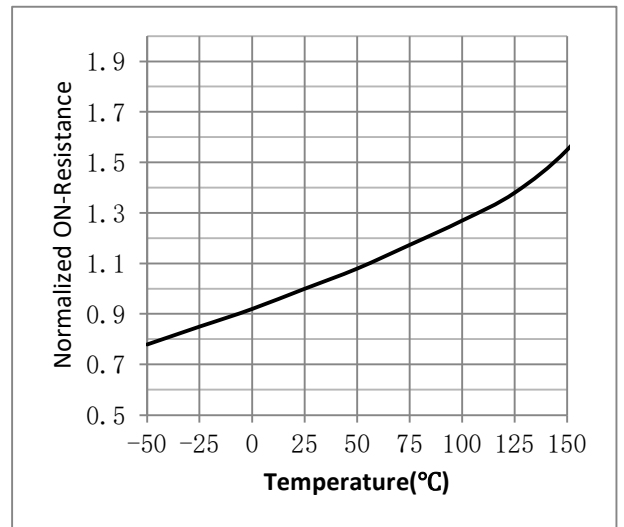


Figure 9. Diode Forward Voltage vs. Current

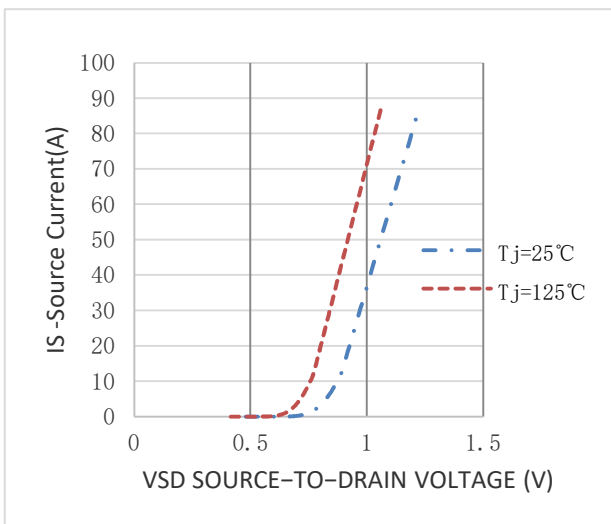


Figure 10. Transfer Characteristics

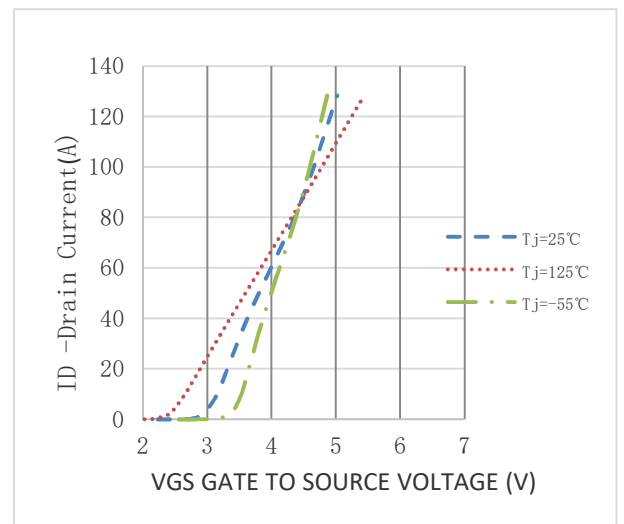


Fig.11 SOA Maximum Safe Operating Area

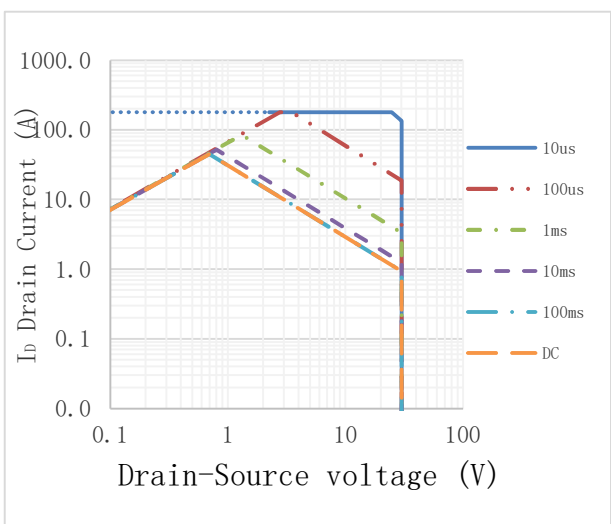
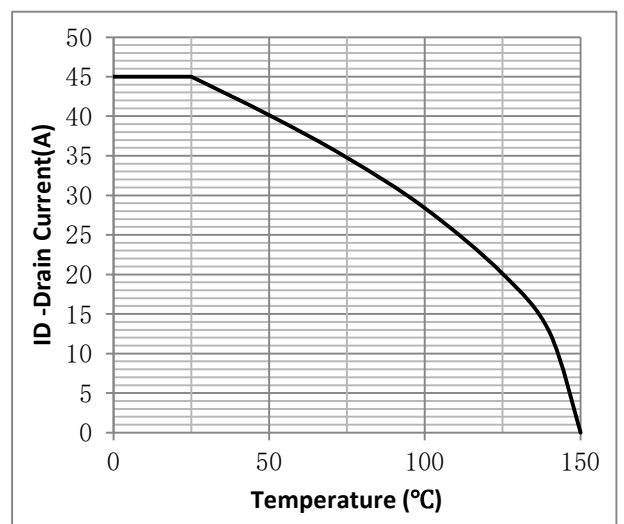


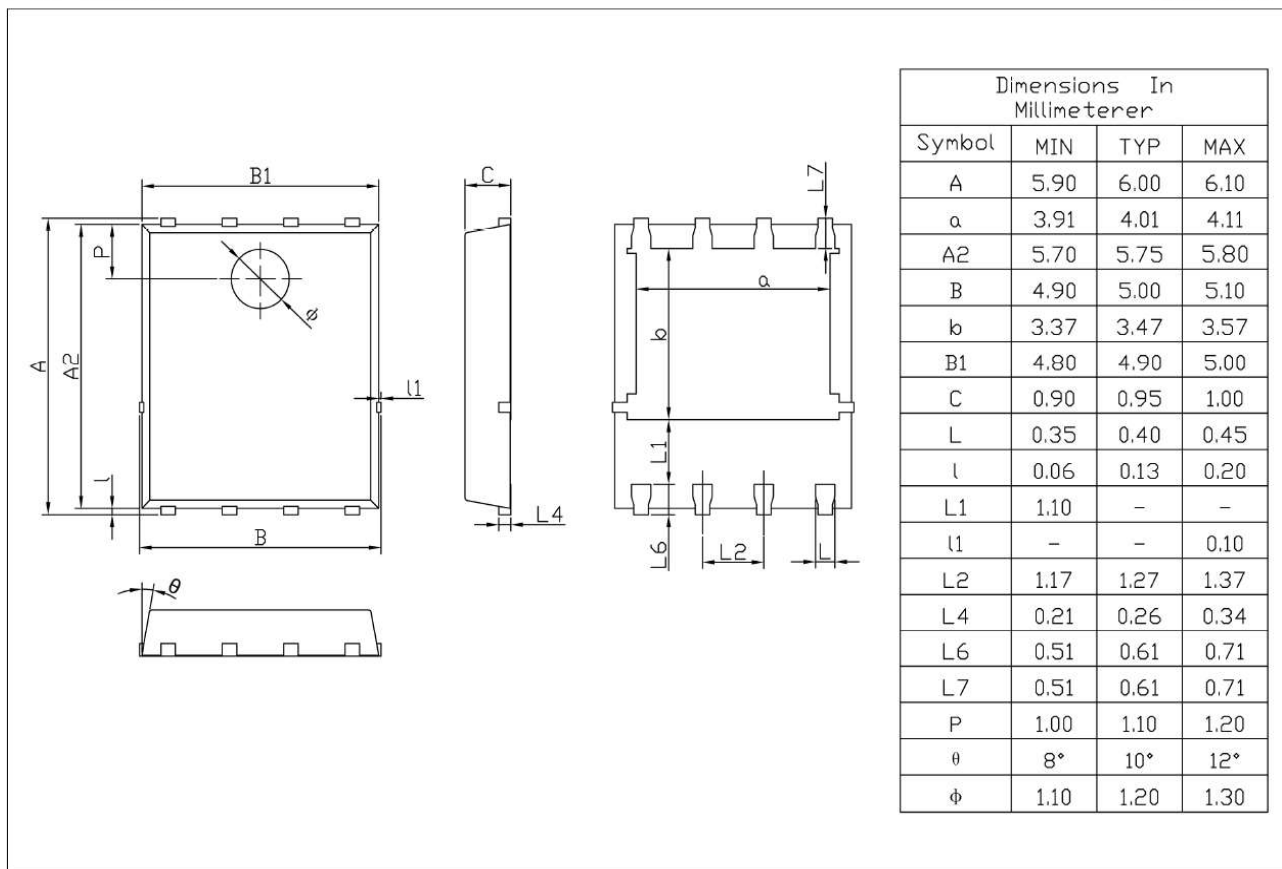
Fig.12 ID vs. Junction Temperature



•DFN5*6 Package Outline

PDFN5 X6

Unit:mm



Note:

- ① Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate;
- ② Practically the current will be limited by PCB, thermal design and operating temperature. $V_{GS}=10V$.

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Revision History

Version	Date	Change
A	2024. 7. 18	New